IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. TAKIKAWA et al

Serial No. (New Continuation Application Under 37 CFR §1.53[b]

of U.S. Application Serial No. 10/631,793)

Filed: On even date herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT

Art Unit: 2811

Examiner: T. DICKEY

<u>INFORMATION DISCLOSURE STATEMENT</u>
<u>UNDER 37 CFR §1.97 & §1.98</u>

MS: PATENT APPLICATIONS
Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

March 30, 2004

Sir:

In the matter of the above-identified application, applicants are attaching hereto, in a form equivalent to Form PTO-1449, a listing of all the art documents of record (cited by the Examiner/submitted by applicants) in the chain of continuing applications (as listed on page 1 of the Specification), pursuant to the duty of disclosure/candor requirements. It is noted that all listed art documents are in the English language.

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified continuation application and, therefore, this IDS meets the requirements pursuant to 37 CFR §1.97(b).

It is understood that since copies of the listed documents should be available in the file wrapper of the prior application(s), copies thereof are not included herewith. consistent with 37 CFR §1.98(d) and pursuant to the guidelines set forth in MPEP §609.

It is submitted, this IDS is in compliance with the rules of practice as well as with USPTO guidelines; therefore, applicants respectfully request that it be entered and duly considered by the USPTO. Also, acknowledgement of such entry as well as of formal consideration thereof by the Examiner is respectfully requested, furthermore.

If any costs are due in accordance with the filing of this Information Disclosure Statement, please charge the same to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (501.38451CC3).

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP

Registration No. 32,392

LNA/dks (703-312-6600)

PTO/SB/08a (08-03)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

	e for form 1449A/PTO			Complete if Known			
				Application Number	R53(b) CONT. of S.N. 10/631,793		
ı	NFORMATION DISC	LOS	SURE	Filing Date	March 30, 2004		
,	STATEMENT BY API	PLIC	ANT	First Named Inventor	TAKIKAWA, KUMIKO		
				Art Unit	2811		
	fuea se manu chaote se na	races	nv)	Examiner Name	T. DICKEY		
Sheet	1	of	2	Attorney Docket Number	501.38451CC3		

			U.S. PATENT	DOCUMENTS	
Examiner Initials'	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 3,974,450	08-10-1976	Cunningham, Jr.	
		US- 4,851,796	07-25-1989	Hendriks	
		US- 5,880,601	03-09-1999	Kanazawa et al	
		US- 5,903,827	05-11-1999	Kennan et al	
		US- 6,522.895	02-18-2003	A. Montalvo	
		US- 6,658,237	12-02-2003	Rozenblit et al	
		US-			

		FOREIG	N PATENT DO	CUMENTS		
Examiner Initials'	Cite No.1	Foreign Patent Document Country Code ³ –Number ⁴ –Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		EP 0823790 A2 and A3	02-01-2003	Nokia Corporation		
		WO 95/02277	19-01-1995	Wireless Access, Inc.		
		EP 0886384 A2	23-12-1998	Lucent Technologies, Inc.		

Examiner	Date	
Signature	Considered	

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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				Complete if Known		
Substitute for form 1449B/PTO				Application Number	R53(b) CONT. of S.N. 10/631,793	
	INFORMATION DISCI	OS	URF	Filing Date	March 30, 2004	
	STATEMENT BY APP			First Named Inventor	TAKIKAWA, KUMIKO	
	0.72227			Art Unit	2811	
_	(use as many sheets as ne	cessa	ry)	Examiner Name	T. DICKEY	
Sheet	2	of	2	Attorney Docket Number	501.38451CC3	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		SHIN, Y.I.S., "MAINTAIN SIGNAL INTEGRITY AT HIGH DIGITAL SPEEDS", Electronic Design, 14 May 1992, Penton Publishing, Cleveland, OH [USA], Vol. 40, No. 10, ppgs. 77-78, 80, 82, 84, 86, 88, 89 and 91.	
		TURINSKY, G., "CHIPTRAEGER FÜR HOCHGESCHWINDIGKEITS-IS", Veb Verlag Technik, Berlin, DE, Vol. 41, No. 1., 1992, ppgs. 25-26, [Germany]	
		FONG, K.L. , "DUAL-BAND HIGH-LINEARITY VARIABLE-GAIN LOW-NOISE AMPLIFIERS FOR WIRELESS APPLICATIONS", ISSCC 1999, pp. 224-225, p.463 (TP13.3, 24 pgs.).	
		STEYAERT, Michiel et al, "A Single-Chip CMOS Transceiver for DCS-1800 Wireless Communications", ISSCC 1998, pp. 48-49, p. 411 (TP3.3, 23 pgs.).	
Examiner Signature		Date Considered	

Examiner		Date	
Signature		Considered	
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^{&#}x27;Applicant's unique citation designation number (optional). 'Applicant is to place a check mark here if English language Translation is attached.

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